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1 Massively parallel approach to pattern recognition
Wang, W.; Iyengar, S.S.; Chen, J.;

Computers and Communications, 1990. Conference Proceedings., Ninth Annual International Phoenix Conference on , 21-23 March 1990

Page(s): 61 -67

[\[Abstract\]](#) [\[PDF Full-Text \(528 KB\)\]](#) **IEEE CNF**
2 Fast template matching based on the normalized correlation by using multiresolution eigenimages
Yoshimura, S.; Kanade, T.;

Intelligent Robots and Systems '94. 'Advanced Robotic Systems and the Real World', IROS '94. Proceedings of the IEEE/RSJ/GI International Conference on , Volume: 3 , 12-16 Sept. 1994

Page(s): 2086 -2093 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(556 KB\)\]](#) **IEEE CNF**
3 High performance algorithms for object recognition problem by multiresolution template matching
Prasad, L.; Iyengar, S.S.;

Tools with Artificial Intelligence, 1995. Proceedings., Seventh International Conference on , 5-8 Nov. 1995

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1 A versatile and powerful chip for real-time motion estimation
Artieri, A.; Jutand, F.;

Acoustics, Speech, and Signal Processing, 1989.

ICASSP-89., 1989 International Conference on , 23-26

May 1989

Page(s): 2453 -2456 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(224 KB\)\]](#) **IEEE CNF**
2 Construction of file management systems from software components
Batory, D.S.; Barnett, J.R.; Roy, J.; Twichell, B.C.; Garza, J.;

Computer Software and Applications Conference, 1989.

COMPSAC 89., Proceedings of the 13th Annual

International , 20-22 Sept. 1989

Page(s): 358 -364

[\[Abstract\]](#) [\[PDF Full-Text \(596 KB\)\]](#) **IEEE CNF**
3 A VLSI architecture for hierarchical scene matching
Venkatesan, R.; Sastry, R.; Ranganathan, N.;

Pattern Recognition, 1992. Vol. IV. Conference D:

Architectures for Vision and Pattern Recognition,

Proceedings., 11th IAPR International Conference on , 30

Aug.-3 Sept. 1992

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